

SFP-1GB-DW-C-75-AO

MSA and TAA 1000Base-DWDM SFP Transceiver (SMF, Tunable, 75km, LC, DOM)

Features

- 1550nm ITU-T C-Band 50GHz Spacing Tunable DWDM SFP+ Transceiver
- Up to 75km Reach
- Data Rate of 1.25Gbps
- PIN PD Receiver with CDR Disabled
- Low Power Consumption: 2.0W at 70 Celsius
- Zero Chirp Transmitters with ILMZ TOSA
- Operating Temperature: 0 to 70 Celsius
- Positive Power Supply Lines: 3.3V
- RoHS Compliant and Lead-Free
- Class 1 Laser Safety Certified



Applications

- 1000Base Ethernet

Product Description

This MSA and TAA compliant SFP transceiver provides 1000Base-DWDM throughput up to 75km over single-mode fiber (SMF) using a tunable wavelength via an LC connector. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S.-made or designated country end products."



ITU Channel Wavelength Guide

ITU Channel	Frequency (THz)	Center Wavelength (nm)	ITU Channel	Frequency (THz)	Center Wavelength (nm)
13	191.30	1567.13	37	193.70	1547.72
13.5	191.35	1566.72	37.5	193.75	1547.32
14	191.40	1566.31	38	193.80	1546.92
14.5	191.45	1565.90	38.5	193.85	1546.52
15	191.50	1565.50	39	193.90	1546.12
15.5	191.55	1565.09	39.5	193.95	1545.72
16	191.60	1564.68	40	194.00	1545.32
16.5	191.65	1564.27	40.5	194.05	1544.92
17	191.70	1563.86	41	194.10	1544.53
17.5	191.75	1563.45	41.5	194.15	1544.13
18	191.80	1563.05	42	194.20	1543.73
18.5	191.85	1562.64	42.5	194.25	1543.33
19	191.90	1562.23	43	194.30	1542.94
19.5	191.95	1561.83	43.5	194.35	1542.54
20	192.00	1561.42	44	194.40	1542.14
20.5	192.05	1561.01	44.5	194.45	1541.75
21	192.10	1560.61	45	194.50	1541.35
21.5	192.15	1560.20	45.5	194.55	1540.95
22	192.20	1559.79	46	194.60	1540.56
22.5	192.25	1559.39	46.5	194.65	1540.16
23	192.30	1558.98	47	194.70	1539.77
23.5	192.35	1558.58	47.5	194.75	1539.37
24	192.40	1558.17	48	194.80	1538.98
24.5	192.45	1557.77	48.5	194.85	1538.58
25	192.50	1557.36	49	194.90	1538.19
25.5	192.55	1556.96	49.5	194.95	1537.79
26	192.60	1556.56	50	195.00	1537.40
26.5	192.65	1556.15	50.5	195.05	1537.00
27	192.70	1555.75	51	195.10	1536.61
27.5	192.75	1555.34	51.5	195.15	1536.22
28	192.80	1554.94	52	195.20	1535.82
28.5	192.85	1554.54	52.5	195.25	1535.43
29	192.90	1554.13	53	195.30	1535.04
29.5	192.95	1553.73	53.5	195.35	1534.64
30	193.00	1553.33	54	195.40	1534.25
30.5	193.05	1552.93	54.5	195.45	1533.86
31	193.10	1552.52	55	195.50	1533.47
31.5	193.15	1552.12	55.5	195.55	1533.07
32	193.20	1551.72	56	195.60	1532.68
32.5	193.25	1551.32	56.5	195.65	1532.29
33	193.30	1550.92	57	195.70	1531.90
33.5	193.35	1550.52	57.5	195.75	1531.51
34	193.40	1550.12	58	195.80	1531.12
34.5	193.45	1549.72	58.5	195.85	1530.72
35	193.50	1549.32	59	195.90	1530.33

35.5	193.55	1548.91	59.5	195.95	1529.94
36	193.60	1548.52	60	196.00	1529.55
36.5	193.65	1548.11	60.5	196.05	1529.16

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Notes
Maximum Supply Voltage	Vcc	0	3.6	V	+3.3V
Optical Receiver Input	PIMAX		5	dBm	Average
Operating Case Temperature	Tc	0	70	°C	
Storage Temperature	Tstg	-40	85	°C	
ESD SFI Pins	ESD1		1	kV	HBM
ESD Except for SFI Pins	ESD2		2	kV	HBM
Data Rate	DR	1.2		Gbps	NRZ

Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Supply Voltage	Vcc	3.135	3.3	3.465	V	+3.3V
Supply Current	Icc			0.63	A	1
Power Consumption				2.0	W	2
Low-Speed Control Pin Logic Levels						
Host_Vcc Range	Host_Vcc	3.14		3.47	V	With ± 5% variation
Tx_Fault, Rx_LOS	VOL	0.0		0.4	V	Note 1
	VOH	Host_Vcc-0.5		Host_Vcc+0.3	V	Note 1
Tx_Disable	VIL	-0.3		0.8	V	Pulled up with 10kΩ to the VccT in the module
	VIH	2.0		VccT +0.3	V	

Notes:

- 0.67A @ Tc<5°C.
- 2.1W @ Tc<5°C.
- Rpullup (Rp) is the pull-up resistor. Active bus termination may be used by the host in place of a pull-up resistor. Pull-ups can be connected to multiple power supplies; however, the host board design shall ensure that no module pin has a voltage exceeding the module. Measures at the host side of the connector.

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Frequency Range		191.35		196.10	THz	50GHz grid, 96 channels
Frequency Accuracy		-2.5		2.5	GHz	EOL
Optical Transmit Power	P _o	0		3	dBm	EOL
Shuttered Output Power				-35	dBm	
Optical Power Stability	ΔP _o	-1		1	dB	All channels, SOL
Side-Mode Suppression Ratio	SMSR	35			dB	±2.5nm, modulated
Spectral Width	Δλ		0.3	0.5	nm	-20dB, modulated
Extinction Ratio	ER	9.0			dB	Filtered, @1.25Gbps
Eye Diagram Compliance		GR-253, ITU-T G.691				
Mask Margin		10			%	
Tuning Speed				10	s	Warmed-up, from any CH to any other CH
Receiver						
Input Operating Wavelength		1525		1575	nm	
Minimum Receiver Sensitivity (Back-to-Back)	P _{rmin}			-19	dBm	@1.25Gbps, 1E ⁻¹² , OSNR>35dB
Maximum Input Power (Overload)	P _{ro}	0			dBm	@1.25Gbps, 1E ⁻¹² , OSNR>35dB
Receiver Reflectance	RL	-27			dB	
LOS Assert		-27			dBm	
LOS De-Assert				-20	dBm	
LOS Hysteresis		0.5		4.0	dB	
LOS Assert Time				100	us	
LOS De-Assert Time				100	us	

SFP+ 2-Wire Interface Requirements

Parameter	Symbol	Min.	Max.	Unit	Conditions
Timing Requirements					
Clock Frequency	fSCL	100	400	kHz	
Clock Pulse Width - Low	tLOW	1.3		µs	
Clock Pulse Width - High	tHIGH	0.6		µs	
Time Bus is Free Before New Transaction Can Start	tBUF	20		µs	Between STOP and START
START Hold time	tHD,STA	0.6		µs	
START Set-Up Time	tSU,STA	0.6		µs	
Data In Hold Time	tHD,DAT	0		µs	
Data In Set-Up Time	tSU,DAT	0.1		µs	
Input Rise Time (100kHz)	tR,100		1000	ns	Note 1
Input Rise Time (400kHz)	tR,400		300	ns	Note 1
Input Fall Time (100kHz)	tF,100		300	ns	Note 1
Input Fall Time (400kHz)	tF,400		300	ns	Note 1
STOP Set-Up Time	tSU,STO	0.6		µs	
Serial Interface Clock Hold Off "Clock Stretching"	T_clock_hold		500	µs	Maximum time the SFP+ may hold the SCL line low before continuing R or W operation
Complete Single or Sequential Write	tWR		40	ms	Complete (up to) 8-Byte Write
Endurance (Write Cycles)		10k		Cycles	@Max. Operating Temperature
Physical Interface					
SCL and SDA	VOL	0.0	0.40	V	Rpullup ² pulled to Host_Vcc. IOL(max) = 3mA.
	VOH	Host_Vcc- 0.5	Host_Vcc + 0.3	V	Rpullup ² pulled to Host_Vcc.
	VIL	-0.3	VccT * 0.3	V	
	VIH	VccT * 0.7	VccT + 0.5	V	
Input Current on SCL and SDA Pins	IL	-10	10	µA	
Capacitance on SCL and SDA Pins	Ci ²		14	pF	
Total Bus Capacitance for SCL and SDA	Cb ³		100	pF	At 400kHz, Rp(max.) = 3.0 kΩ At 100kHz, Rp(max.) = 8.0 kΩ
			290	pF	At 400kHz, Rp(max.) = 1.1 kΩ At 100kHz, Rp(max.) = 2.75 kΩ

Notes:

- From (VIL, Max -0.15) to (VIH, Min +0.15).
- Rpullup (Rp) is the pull-up resistor. Active bus termination may be used by the host in place of a pull-up resistor. Pull-ups can be connected to multiple power supplies; however, the host board design shall ensure that no module pin has a voltage exceeding the module. Measured at the host side of the connector. VccT/R+0.5V nor requires the module to sink more than 3.0mA current.

3. C_i is the capacitance looking into the module SCL and SDA pins.
4. C_b is the total bus capacitance on the SCL or SDA bus.

SFP+ Timing Requirements

Parameter	Symbol	Min.	Max.	Unit	Conditions
Tx_Disable Assert Time	T_off		100	μs	Rising edge of Tx_Disable to fall of output signal below 10% of nominal.
Tx_Disable Negate Time	T_on		2	ms	Falling edge of Tx_Disable to rise output signal above 90% of nominal. This only applies in normal operation, not during start up or fault recovery.
Time to Initialize 2-Wire Interface	T_2w_start_up		300	ms	From power on or hot plug after the supply meeting.
Time to Initialize Cooled Module and Time to Power Up a Cooled Module to Power Level II	T_start_up_cooled		90	sec	From power supplies meeting or hot plug, or Tx_Disable negated during power up or Tx_Fault recovery, until cooled Power Level II part during fault recovery is fully operational. Also, from stop bit-low-to-high SDA transition enabling Power Level II until cooled module is fully operational.
Tx_Fault Assert for Cooled Module	Tx_fault_on_cooled		1	ms	From occurrence of fault to assertion of Tx_Fault.
Tx_Fault Reset	T_reset	10		μs	Time Tx_Disable must be held high to reset Tx_Fault.
Rx_LOS Assert Delay	T_los_on		100	μs	From occurrence of loss of signal to assertion of Rx_LOS.
Rx_LOS Negate Delay	T_los_off		100	μs	From occurrence of presence of signal to negation of Rx_LOS.
Maximum Current Ramp on Power Supply					
Icc Instantaneous Peak Current			600	mA	Notes 1 & 2
Icc Sustained Peak Current			500	mA	Notes 1 & 2

Notes:

1. The maximum currents are the allowed currents for each power supply VccT or VccR. Therefore, the total module peak currents can be twice this value. The instantaneous peak current is allowed to exceed the specified maximum current capacity of the connector contact for a short period.
2. Not to exceed the sustained peak limit for the more than 50μs. May exceed this limit for shorter durations.

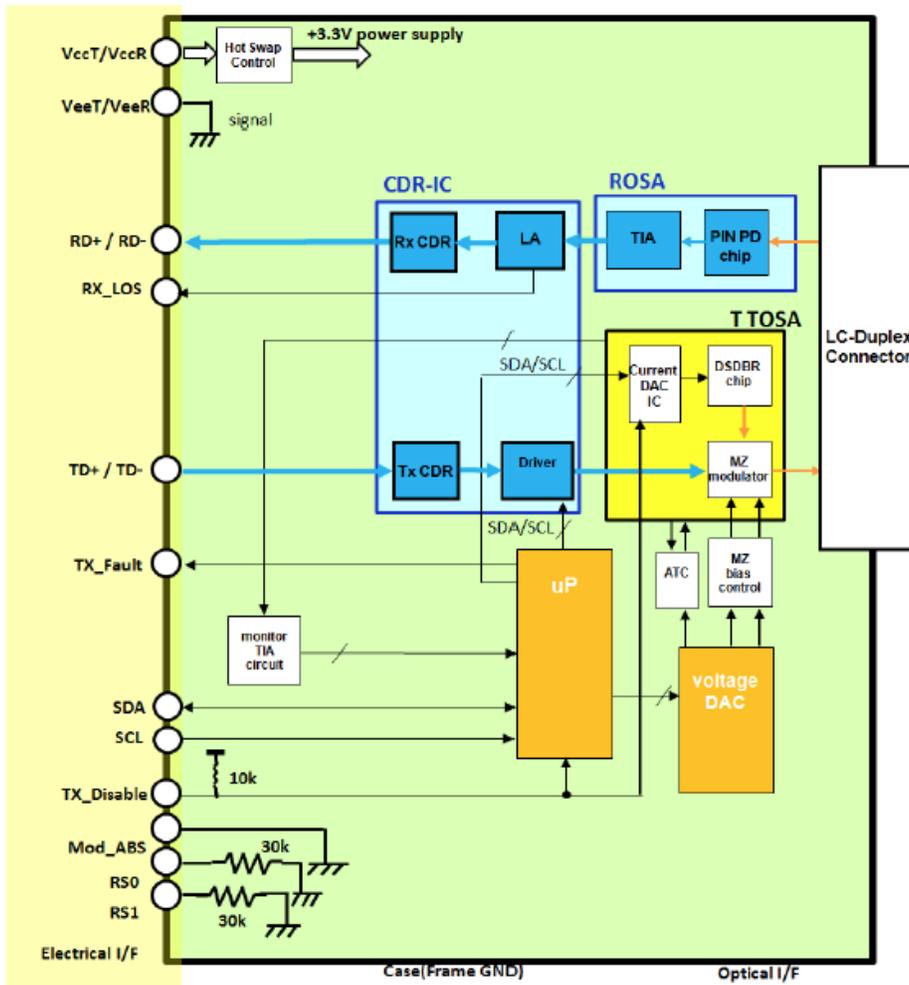
Pin Descriptions

Pin	Logic	Symbol	Power Sequence Order	Name/Description	Notes
1		VeeT	1	Module Transmitter Ground.	1
2	LVTTTL-O	Tx_Fault	3	Module Transmitter Fault.	2
3	LVTTTL-I	Tx_Disable	3	Transmitter Disable. Turn off the laser output.	3
4	LVTTTL-I/O	SDA	3	2-Wire Serial Interface Data.	
5	LVTTTL-I/O	SCL	3	2-Wire Serial Interface Clock.	
6		MOD_ABS	3	Module Absent. Connected to VeeT or VeeR in the module.	4
7	LVTTTL-I	RS0	3	N/A. 30kΩ pull-down inside the module.	
8	LVTTTL-O	Rx_LOS	3	Receiver Loss of Signal Indicator.	2
9	LVTTTL-I	RS1	3	N/A. 30kΩ pull-down inside the module.	
10		VeeR	1	Module Receiver Ground.	1
11		VeeR	1	Module Receiver Ground.	1
12	CML-O	RD-	3	Receiver Inverted Data Output (SFI).	
13	CML-O	RD+	3	Receiver Non-Inverted Data Output (SFI).	
14		VeeR	1	Module Receiver Ground.	1
15		VccR	2	Module Receiver 3.3V Supply.	5
16		VccT	2	Module Transmitter 3.3V Supply.	5
17		VeeT	1	Module Transmitter Ground.	1
18	CML-I	TD+	3	Transmitter Non-Inverted Data Output (SFI).	
19	CML-I	TD-	3	Transmitter Inverted Data Output (SFI).	
20		VeeT	1	Module Transmitter Ground.	1

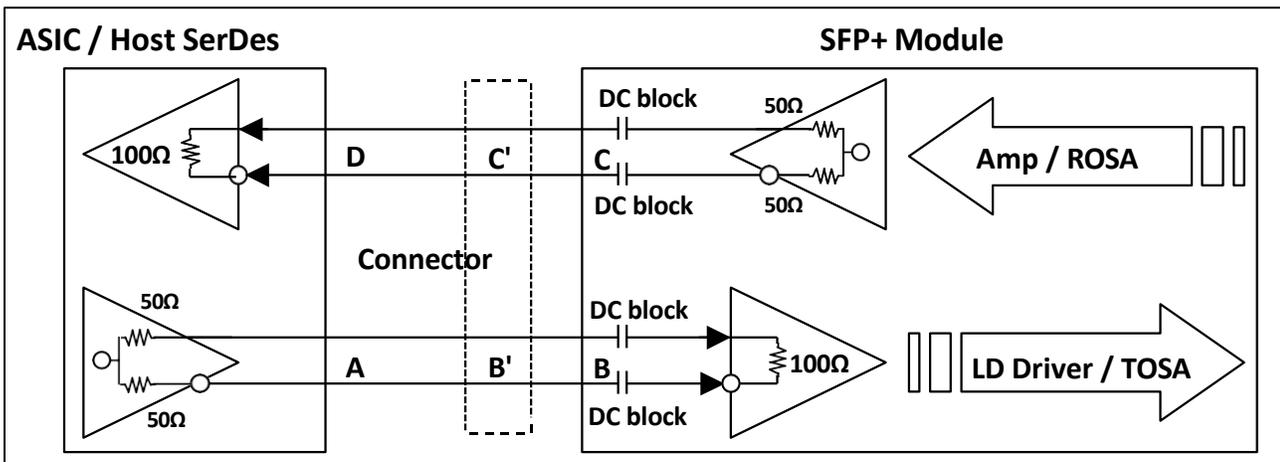
Notes:

1. The module signal ground pins, VeeR and VeeT, are isolated from the module case.
2. This pin is an open drain output pin and shall be pulled up with 4.7kΩ to 10kΩ to the Host_Vcc on the host board. Pull-ups can be connected to multiple power supplies; however, the host board design shall ensure that no module pin has a voltage exceeding the module $V_{ccT}/R+0.5V$.
3. This pin is an input pin with a 10kΩ pull-up to the VccT in the module.
4. This pin shall be pulled up with 4.7kΩ to 10kΩ to the Host_Vcc on the host board.
5. VccT and VccR are tied together inside the module.

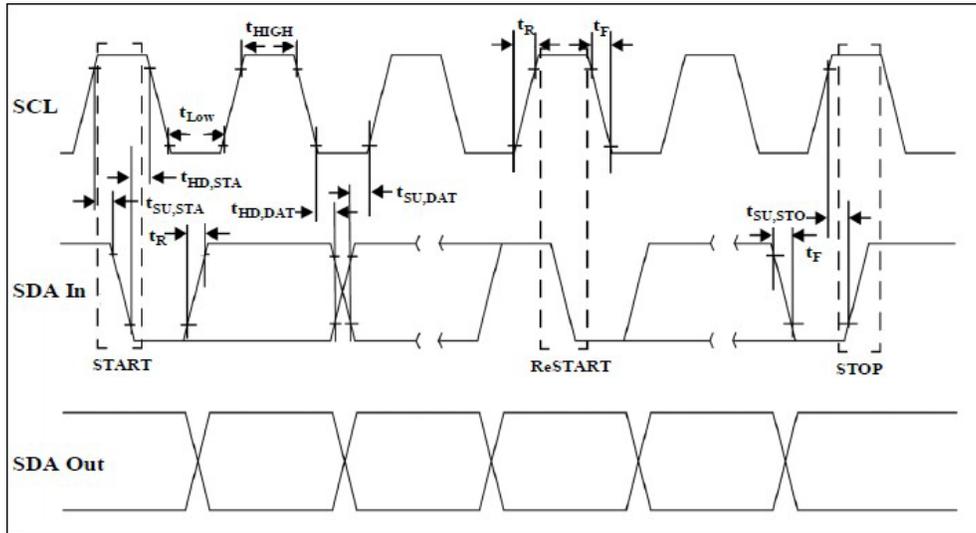
Recommended Circuit Schematic



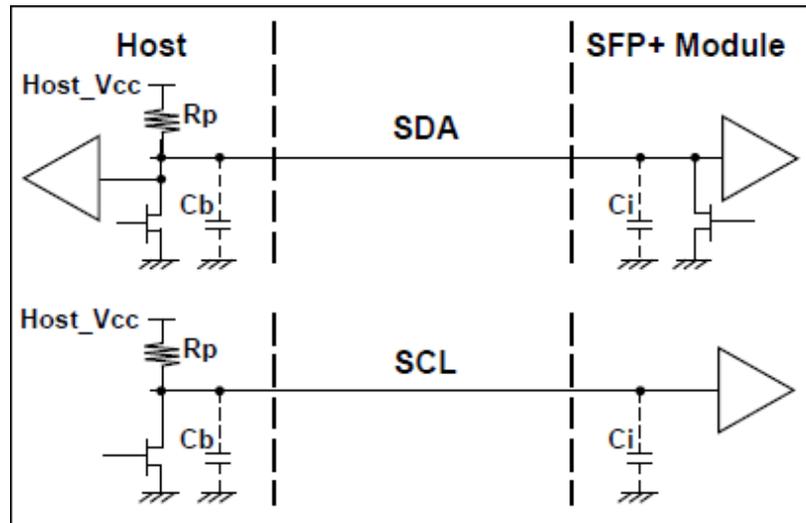
SFI Data Interface



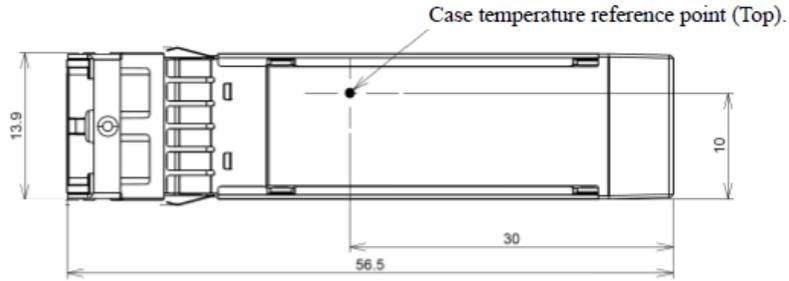
SFP+ Timing Diagram



Physical Interface

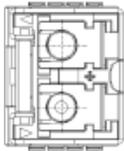


Case Temperature Reference Point (Top)

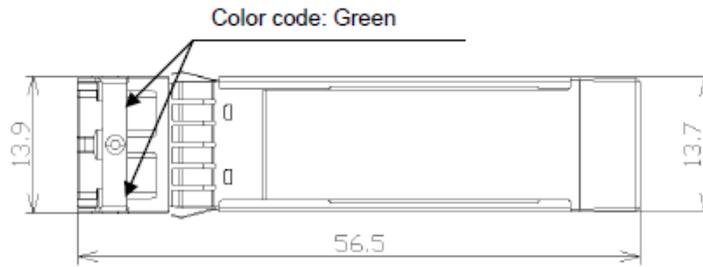


Mechanical Specifications

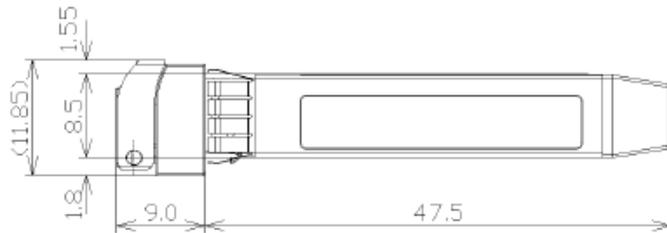
Side view



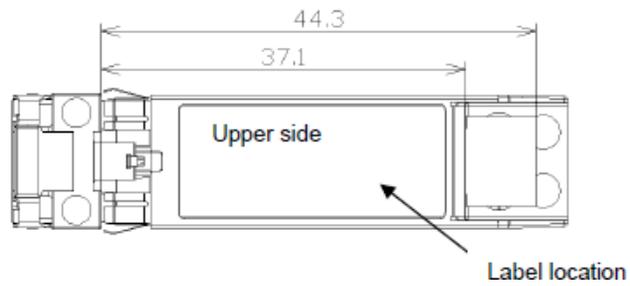
Top view



Side view



Bottom view



About AddOn Networks

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is engrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.



U.S. Headquarters

Email: sales@addonnetworks.com

Telephone: +1 877.292.1701

Fax: 949.266.9273

Europe Headquarters

Email: salesupportemea@addonnetworks.com

Telephone: +44 1285 842070