# **addon**

#### QSFP28-100GB-SR2-MX-AO

Mellanox® Compatible TAA 100GBase-SR2 QSFP28 Transceiver (MMF, 850nm, 100m, MPO, DOM)

#### **Features**

- Supports 106.25Gbps
- PIN and TIA array on the Receiver Side
- MPO12 PC/APC Connector
- Single 3.3V Power Supply
- Up to 100m Transmission on MMF OM4
- Compliant to SFF-8665 MSA
- VCSEL Transmitter
- Up to 70m Transmission on MMF OM3
- Operating Temperature: 0 to 70 Celsius
- Class 1 Laser
- RoHS Compliant and Lead-Free



### **Applications**

• 100GBase Ethernet

#### **Product Description**

This Mellanox® QSFP28 transceiver provides 100GBase-SR2 throughput up to 100m over OM4 multi-mode fiber (MMF) using a wavelength of 850nm via an MPO connector. It is guaranteed to be 100% compatible with the equivalent Mellanox® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



## **Absolute Maximum Ratings**

Parameter		Symbol	Min.	Тур.	Max.	Unit	Notes
Maximum Supply	Voltage	Vcc	-0.5	3.3	3.6	V	
Storage Temperat	ure	T <sub>Stg</sub>	-40		85	°C	
Relative Humidity		RH	5		85	%	
Operating Case Temperature		Тс	0		70	°C	
Power Supply Total Current					1212	mA	
AC Coupling Internal Capacitor				0.1		μV	
Modulation Format				PAM4			
Fiber Length	OM3				70	m	
	OM4				100	m	

#### **Electrical Characteristics**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Module Supply Voltage	Vcc	3.135	3.3	3.6	V	
Power Supply Noise				25	mVp-p	1
Receiver Differential Data Output Load			100		Ω	
Power Consumption				4	W	

#### Notes:

1. Power Supply Noise is defined as the peak-to-peak noise amplitude over the frequency range at the host supply side of the recommended power supply filter with the module and recommended filter in place. Voltage levels including peak-to-peak noise are limited to the recommended operating range of the associated power supply.

**High-Speed Electrical Characteristics** 

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
@TP1 Test Point						
Signaling Rate Per Lane			25.78125 ± 100ppm			
Differential Pk-Pk Input Voltage Tolerance	TP1a	900			mV	1
Differential Input Return Loss	TP1	Equation (83E-5)			dB	2
Common- to Differential-Mode Conversion Return Loss	TP1	Equation (83E-6)			dB	2
Differential Termination Mismatch	TP1			10	%	
Single-Ended Voltage Tolerance Range	TP1a	-0.4		3.3	V	
DC Common-Mode Output Voltage	TP1	-350		2850	mV	3
Module Stressed Input Test	TP1a					4
Eye Width			0.46		UI	
Eye Height			95		mV	
Applied Pk-Pk Sinusoidal Jitter			Table 88-13			4
@TP4 Test Point						
Signaling Rate Per Lane			25.78125 ± 100ppm			
AC Common-Mode Output Voltage (RMS)	TP4			17.5	mV	
Differential Pk-Pk Output Voltage	TP4			900	mV	
Eye Width	TP4	0.57			UI	
Eye Height (Differential)	TP4	228			mV	
Vertical Eye Closure	TP4			5.5	dB	
Differential Output Return Loss	TP4	Equation (83E-2)				5
Common- to Differential-Mode Conversion Return Loss	TP4	Equation (83E-3)				5
Differential Termination Mismatch	TP4			10	%	
Transition Time (20-80%)	TP4			12	ps	
DC Common-Mode Voltage	TP4	-350		2850	mV	2

## Notes:

- 1. Equation (83E-5) and Equation (83E-6) refer to IEEE 802.3-2018.
- 2. DC common-mode voltage is generated by the host. Specification includes the effects of ground offset voltage.
- 3. Meets BER specified in 120E.1.1 of IEEE 802.3-2018.
- 4. Table 88-13 refers to IEEE 802.3-2018.
- 5. Equation (83E-2) and Equation (83E-3) refer to IEEE 802.3-2018.

**Optical Characteristics** 

GTP2 Test Point         Signaling Rate Per Lane         DR         26.5625 ± 100ppm         GBd           Center Wavelength         λ         840         868         nm           RMS Spectral Width         Δλrms         0.6         nm           Average Launch Power Per Lane         Pavg         -6.5         4         dBm           Outer Optical Modulation Amplitude (OMAouter) Per Lane         POMA         -4.5         3         dBm           Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane         TDECQ         -5.9         dB         dB           TDECQ-10log₁₀ (Ceq) Per Lane         TDECQ-10log₁₀ (Ceq)         4.5         dB           Average Launch Power of Off Transmitter Per Lane         Poff         -30         dBm           Extinction Ratio Per Lane         ER         3         dB         dB/Hz           Optical Return Loss Tolerance         ORLT         12         dB           Encircled Flux         EF         ≥86% at 19μm         -128         dB	1 2 3
Center Wavelength       λ       840       868       nm         RMS Spectral Width       △λrms       0.6       nm         Average Launch Power Per Lane       Pavg       -6.5       4       dBm         Outer Optical Modulation Amplitude (OMAouter) Per Lane       POMA       -4.5       3       dBm         Launch Power in OMAouter Minus TDECQ       OMA-TDECQ       -5.9       dBm         Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane       TDECQ       4.5       dB         TDECQ-10log₁₀ (Ceq) Per Lane       TDECQ-10log₁₀ (Ceq)       4.5       dB         Average Launch Power of Off Transmitter Per Lane       Poff       -30       dBm         Extinction Ratio Per Lane       ER       3       dB         RIN₁₂OMA       RIN₁₂OMA       -128       dB/Hz         Optical Return Loss Tolerance       ORLT       12       dB         Encircled Flux       EF       ≥86% at 19μm	2
RMS Spectral Width △λrms 0.6 nm  Average Launch Power Per Lane Pavg -6.5 4 dBm  Outer Optical Modulation Amplitude (OMAouter) Per Lane  Launch Power in OMAouter Minus TDECQ OMA-TDECQ -5.9 dBm  Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane  TDECQ-10log₁₀ (Ceq) Per Lane  TDECQ-10log₁₀ (Ceq) Per Lane  Extinction Ratio Per Lane  RIN₁₂OMA  RIN₁₂OMA  Coptical Return Loss Tolerance  RMS Spectral Width  A dBm  OMA  DOMA  DOMA  DOMA  DOBA  DOBA	2
Average Launch Power Per Lane       Pavg       -6.5       4       dBm         Outer Optical Modulation Amplitude (OMAouter) Per Lane       POMA       -4.5       3       dBm         Launch Power in OMAouter Minus TDECQ       OMA-TDECQ       -5.9       dBm         Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane       TDECQ       4.5       dB         TDECQ-10log₁₀ (Ceq) Per Lane       TDECQ-10log₁₀ (Ceq)       4.5       dB         Average Launch Power of Off Transmitter Per Lane       Poff       -30       dBm         Extinction Ratio Per Lane       ER       3       dB /Hz         Optical Return Loss Tolerance       ORLT       12       dB         Encircled Flux       EF       ≥86% at 19μm	2
Outer Optical Modulation Amplitude (OMAouter) Per Lane         Launch Power in OMAouter Minus TDECQ       OMA-TDECQ       -5.9       dBm         Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane       TDECQ       4.5       dB         TDECQ-10log₁₀ (Ceq) Per Lane       TDECQ-10log₁₀ (Ceq)       4.5       dB         Average Launch Power of Off Transmitter Per Lane       Poff       -30       dBm         Extinction Ratio Per Lane       ER       3       dB         RIN₁₂OMA       RIN₁₂OMA       -128       dB/Hz         Optical Return Loss Tolerance       ORLT       12       dB         Encircled Flux       EF       ≥86% at 19μm	
(OMAouter) Per Lane       dBm         Launch Power in OMAouter Minus TDECQ       OMA-TDECQ       -5.9       dBm         Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane       TDECQ       4.5       dB         TDECQ-10log₁₀ (Ceq) Per Lane       TDECQ-10log₁₀ (Ceq)       4.5       dB         Average Launch Power of Off Transmitter Per Lane       Poff       -30       dBm         Extinction Ratio Per Lane       ER       3       dB         RIN₁₂OMA       RIN₁₂OMA       -128       dB/Hz         Optical Return Loss Tolerance       ORLT       12       dB         Encircled Flux       EF       ≥86% at 19μm	
Transmitter and Dispersion Eye Closure for PAM4 (TDECQ) Per Lane       TDECQ-10log₁0 (Ceq) Per Lane       4.5 dB         Average Launch Power of Off Transmitter Per Lane       Poff       -30 dBm         Extinction Ratio Per Lane       ER       3 dB         RIN₁₂OMA       RIN₁₂OMA       -128 dB/Hz         Optical Return Loss Tolerance       ORLT       12 dB         Encircled Flux       EF       ≥86% at 19μm	3
for PAM4 (TDECQ) Per Lane       TDECQ-10log₁0 (Ceq) Per Lane       TDECQ-10log₁0 (Ceq)       4.5       dB         Average Launch Power of Off Transmitter Per Lane       Poff       -30       dBm         Extinction Ratio Per Lane       ER       3       dB         RIN₁₂OMA       RIN₁₂OMA       -128       dB/Hz         Optical Return Loss Tolerance       ORLT       12       dB         Encircled Flux       EF       ≥86% at 19μm	3
Average Launch Power of Off Transmitter       Poff       -30       dBm         Per Lane       ER       3       dB         Extinction Ratio Per Lane       ER       3       dB         RIN₁₂OMA       RIN₁₂OMA       -128       dB/Hz         Optical Return Loss Tolerance       ORLT       12       dB         Encircled Flux       EF       ≥86% at 19μm	3
Per Lane         Extinction Ratio Per Lane         ER         3         dB           RIN₁₂OMA         RIN₁₂OMA         -128         dB/Hz           Optical Return Loss Tolerance         ORLT         12         dB           Encircled Flux         EF         ≥86% at 19μm	
RIN₁₂OMA         RIN₁₂OMA         -128         dB/Hz           Optical Return Loss Tolerance         ORLT         12         dB           Encircled Flux         EF         ≥86% at 19μm	
Optical Return Loss Tolerance     ORLT     12     dB       Encircled Flux     EF     ≥86% at 19μm	
Encircled Flux EF ≥86% at 19μm	
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≤30% at 4.5μm	4
@TP3 Test Point	
Signaling Rate Per Lane         DR         26.5625 ± 100ppm         GBd	
Center Wavelength         λ         840         868         nm	
Damage Threshold   5   dBm	5
Average Receiver Power Per Lane -8.4 4 dBm	6
Receiver Power Per Lane (OMAouter) 3 dBm	
Receiver Reflectance -12 dB	
Stressed Receiver (OMAouter) Per Lane -3.4 dB	7
LOS Assert LOSA -17 dBm	
LOS De-Assert LOSD -11 dBm	
Receiver Sensitivity (OMAouter) Per Lane Max. (-6.5, SECQ-7.9)	8
Conditions of Stressed Receiver Sensitivity Test	
Stressed Eye Closure for PAM4 4.5 dB (SECQ) Per Lane Under Test	9
SECQ-10log10(Ceq) Per Lane Under Test 4.5 dB	9, 3
OMAouter of Each Aggressor Lane 3 dBm	9, 10

## Notes:

- 1. RMS spectral width is the standard deviation of the spectrum.
- 2. Even if the TDECQ < 1.4dB, the OMAouter (minimum) must exceed this value.
- 3. Ceq is a coefficient defined in 121.8.5.3, which accounts for the reference equalizer noise enhancement.

- 4. If measured into type A1a.2 or type A1a.3, or A1a.4, 50µm fiber, in accordance with IEC 61280-1-4.
- 5. The receiver shall be able to tolerate, without damage, continuous exposure to an optical input signal having this average power level on one lane. The receiver does not have to operate correctly at this input power.
- 6. Average receive power, per lane (minimum), is informative and not the principal indicator of signal strength. A received power below this value cannot be compliant; however, a value above this does not ensure compliance.
- 7. Measured with conformance test signal at TP3 (see 138.8.10) for the BER specified in 138.1.1.
- 8. Receiver sensitivity is informative and is defined for a transmitter with a value of SECQ up to 4.5dB with BER $< 2.4 \times 10^{-4}$  without FEC in PRBS  $2^{31}$ -1.
- 9. These test conditions are for measuring stressed receiver sensitivity. They are not characteristics of the receiver.
- 10. Ceq is a coefficient defined in 121.8.5.3, which accounts for the reference equalizer noise enhancement.

### **Pin Descriptions**

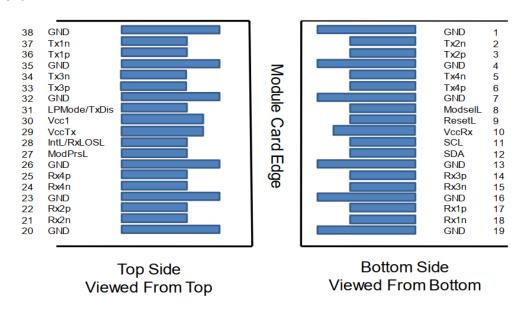
Pin	Logic	Symbol	Name/Description	Plug Sequence	Notes
1		GND	Module Ground.	1	1
2	CML-I	Tx2-	Transmitter Inverted Data Input (25G NRZ or 53G PAM4).	3	
3	CML-I	Tx2+	Transmitter Non-Inverted Data Input (25G NRZ or 53G PAM4).	3	
4		GND	Module Ground.	1	1
5	CML-I	Tx4-	Transmitter Inverted Data Input (25G NRZ or Not Used).	3	
6	CML-I	Tx4+	Transmitter Non-Inverted Data Input (25G NRZ or Not Used).	3	
7		GND	Module Ground.	1	1
8	LVTTL-I	ModSelL	Module Select.	3	
9	LVTTL-I	ResetL	Module Reset.	3	
10		VccRx	+3.3V Receiver Power Supply.	2	2
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock.	3	
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data.	3	
13		GND	Module Ground.	1	1
14	CML-O	Rx3+	Receiver Non-Inverted Data Output (25G NRZ or Not Used).	3	
15	CML-O	Rx3-	Receiver Inverted Data Output (25G NRZ or Not Used).	3	
16		GND	Module Ground.	1	1
17	CML-O	Rx1+	Receiver Non-Inverted Data Output (25G NRZ or 53G PAM4).	3	
18	CML-O	Rx1-	Receiver Inverted Data Output (25G NRZ or 53G PAM4).	3	
19		GND	Module Ground.	1	1
20		GND	Module Ground.	1	1
21	CML-O	Rx2-	Receiver Inverted Data Output (25G NRZ or 53G PAM4).	3	
22	CML-O	Rx2+	Receiver Non-Inverted Data Output (25G NRZ or 53G PAM4).	3	

23		GND	Module Ground.	1	1
24	CML-O	Rx4-	Receiver Inverted Data Output (25G NRZ or Not Used).	3	
25	CML-O	Rx4+	Receiver Non-Inverted Data Output (25G NRZ or Not Used).	3	
26		GND	Module Ground.	1	1
27	LVTTL-O	ModPrsL	Module Present.	3	
28	LVTTL-O	IntL	Interrupt.	3	
29		VccTx	+3.3V Transmitter Power Supply.	2	2
30		Vcc1	+3.3V Power Supply.	2	2
31	LVTTL-I	LPMode	Low-Power Mode.	3	
32		GND	Module Ground.	1	1
33	CML-I	Tx3+	Transmitter Non-Inverted Data Input (25G NRZ or Not Used).	3	
34	CML-I	Tx3-	Transmitter Inverted Data Input (25G NRZ or Not Used).	3	
35		GND	Module Ground.	1	1
36	CML-I	Tx1+	Transmitter Non-Inverted Data Input (25G NRZ or 53G PAM4).	3	
37	CML-I	Tx1-	Transmitter Inverted Data Input (25G NRZ or 53G PAM4).	3	
38		GND	Module Ground.	1	1

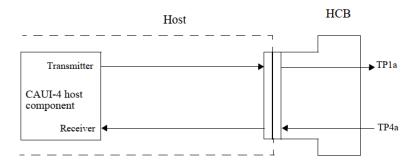
#### **Notes:**

- 1. GND is the symbol for signal and supply (power) common for the module. All are common within the module, and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.
- 2. VccRx, Vcc1, and VccTx are applied concurrently and maybe internally connected within the module in any combination. Vcc contacts in SFF-8662 and SFF-8672 each have a steady state current rating of 1A.

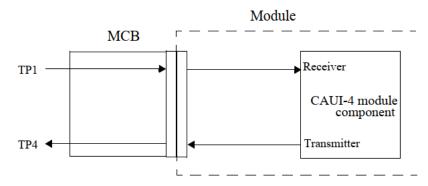
#### **Pin-Out Definitions**



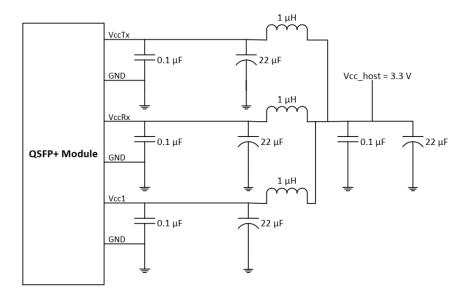
# IEEE 802.3bm Compliance Points TP1a, TP4a



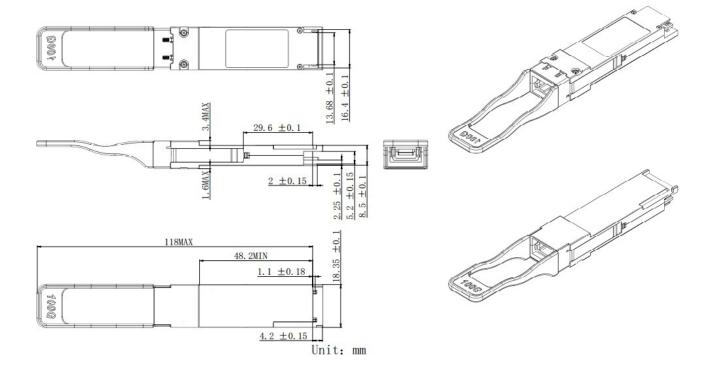
# IEEE 802.3bm Compliance Points TP1, TP4



# **Host Board Power Supply Filter**



# **Mechanical Specifications**



#### **About AddOn Networks**

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is in engrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.











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